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Operation And Modeling Of The Mos Transistor

Operation and Modeling of the MOS Transistor-Yannis Tsividis 1999 Extensively revised and updated, this, the second edition of the highly praised text Operation and Modeling of The MOS Transistor, has become a standard in academia and industry. The book provides a thorough treatment of the MOS transistor-the key element of most modern microelectronic chips. KEY FEATURES .Unified, careful ...

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This is the first book dedicated to the next generation of MOSFET models. Addressed to circuit designers with an in-depth treatment that appeals to device specialists, the book presents a fresh view of compact modeling, having completely abandoned the regional modeling approach.Both an overview of the basic physics theory required to build compact MOSFET models and a unified treatment of inversion-charge and surface-potential models are provided. The needs of digital, analog and RF designers as regards the availability of simple equations for circuit designs are taken into account. Compact expressions for hand analysis or for automatic synthesis, valid in all operating regions, are presented throughout the book. All the main expressions for computer simulation used in the new generation compact models are derived.Since designers in advanced technologies are increasingly concerned with fluctuations, the modeling of fluctuations is strongly emphasized. A unified approach for both space (matching) and time (noise) fluctuations is introduced.

Written by an award-winning educator and researcher, the sixteen experiments in this book have been extensively class-tested and fine-tuned. This lab manual, like no other, provides an exciting, active exploration of concepts and measurements and encourages students to tinker, experiment, and become creative on their own. This benefits their further study and subsequent professional work. The manual includes self-contained background for all electronics experiments, so that the lab can be run concurrently with any circuits or electronics course, at any level. It uses circuits in real applications which students can relate to, in order to motivate them and convince them that what they learn is for real. As a result, the material is not only made interesting, but helps motivate further study in circuits, electronics, communications and semiconductor devices. EXTENSIVE INSTRUCTOR RESOURCES: * "Putting the Lab Together" is an extensive resource for instructors who are considering starting a lab based on this book. Includes an overview of a typical lab station, suggestions for choosing measurement equipment, equipment list with relevant information, and detailed information on parts required. This resource is openly available. * "Instructor's Manual" includes hints for choosing lab TAs, hints on how to run the lab experiments, guidelines for shortening or combining experiments, answers to experiment questions, and suggestions for projects and exams. This manual is available to instructors who adopt the book.

Most of the recent texts on compact modeling are limited to a particular class of semiconductor devices and do not provide comprehensive coverage of the field. Having a single comprehensive reference for the compact models of most commonly used semiconductor devices (both active and passive) represents a significant advantage for the reader. Indeed, several kinds of semiconductor devices are routinely encountered in a single IC design or in a single modeling support group. Compact Modeling includes mostly the material that after several years of IC design applications has been found both theoretically sound and practically significant. Assigning the individual chapters to the groups responsible for the definitive work on the subject assures the highest possible degree of expertise on each of the covered models.

Circuit simulation is essential in integrated circuit design, and the accuracy of circuit simulation depends on the accuracy of the transistor model. BSIM3v3 (BSIM for Berkeley Short-channel IGFET Model) has been selected as the first MOSFET model for standardization by the Compact Model Council, a consortium of leading companies in semiconductor and design tools. In the next few years, many fabless and integrated semiconductor companies are expected to switch from dozens of other MOSFET models to BSIM3. This will require many device engineers and most circuit designers to learn the basics of BSIM3. MOSFET Modeling & BSIM3 User's Guide explains the detailed physical effects that are important in modeling MOSFETs, and presents the derivations of compact model expressions so that users can understand the physical meaning of the model equations and parameters. It is the first book devoted to BSIM3. It treats the BSIM3 model in detail as used in digital, analog and RF circuit design. It covers the complete set of models, i.e., I-V model, capacitance model, noise model, parasitics model, substrate current model, temperature effect model and non quasi-static model. MOSFET Modeling & BSIM3 User's Guide not only addresses the device modeling issues but also provides a user's guide to the device or circuit design engineers who use the BSIM3 model in digital/analog circuit design, RF modeling, statistical modeling, and technology prediction. This book is written for circuit designers and device engineers, as well as device scientists worldwide. It is also suitable as a reference for graduate courses and courses in circuit design or device modelling. Furthermore, it can be used as a textbook for industry courses devoted to BSIM3. MOSFET Modeling & BSIM3 User's Guide is comprehensive and practical. It is balanced between the background information and advanced discussion of BSIM3. It is helpful to experts and students alike.

Analysis and Design of MOSFETs: Modeling, Simulation, and Parameter Extraction is the first book devoted entirely to a broad spectrum of analysis and design issues related to the semiconductor device called metal-oxide semiconductor field-effect transistor (MOSFET). These issues include MOSFET device physics, modeling, numerical simulation, and parameter extraction. The discussion of the application of device simulation to the extraction of MOSFET parameters, such as the threshold voltage, effective channel lengths, and series resistances, is of particular interest to all readers and provides a valuable learning and reference tool for students, researchers and engineers. Analysis and Design of MOSFETs: Modeling, Simulation, and Parameter Extraction, extensively referenced, and containing more than 180 illustrations, is an innovative and integral new book on MOSFETs design technology.