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Agenda | Quick reminder about ARM platforms | User perspective: booting with the Device Tree | Basic Device Tree syntax and compilation | Simple

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example of Device Tree fragment |
Overall organization of a Device Tree |
Examples of Device Tree usage |
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The Device Tree Blob is produced by
the compiler, and is the binary that
gets loaded by the bootloader and
parsed by the kernel at boot time.

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larch/arm/boot/dts/Makefile lists which DTBs should be generated at build time.

```
dtb-$(CONFIG_ARCH_MVEBU) +=  
armada-370-db.dtb /  
armada-370-mirabox.dtb /... Free  
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```

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Device tree compiler and its source code located at scripts/dtc/. On ARM all device tree source are located at /arch/arm/boot/dts/. The Device Tree Blob(.dtb) is produced by the compiler, and it is the binary that gets loaded by the bootloader and parsed by the kernel at boot time.

~~Device Tree Tutorial (ARM) - Linux Kernel For Newbies~~

On top of that, each board has its own set of external components. The result is a wild forest of header files, patches and special configuration parameters in the kernel tree, each combination matching a specific board with a specific chip containing an ARM processor.

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The device tree is a tree structure with nodes that describe the physical devices in the system that cannot be dynamically detected by software.

The nodes are organized in a hierarchical parent/child relationship.

This figure is a representation of a simple device tree, describing the platform type, CPU and memory.

Nodes are organized

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